

selectively connect the first and negative signal element of the differential buses, respectively through individual ones of the two resistors, to the negative voltage source.

33. (New) A device as in claim 32 wherein the resistive circuit connecting the first and negative signal element of the differential buses to each other further comprises two series connected 50 ohm resistors.

34. (New) A device as in claim 32 wherein the switch circuit located between the resistive circuit and a negative voltage source further comprises a field effect transistor.

35. (New) A device as in claim 32 wherein the negative voltage source further comprises -2vdc.

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REMARKS

1. Claims 3, 4 and 12 are amended. Claim 1 is cancelled. Claims 19-35 are new. A marked-up version of the amended claims is attached hereto.

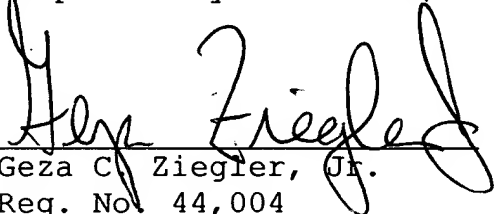
2. It is submitted that claims 3, 4 and 12 are not anticipated by Decuir under 35 U.S.C. §102(b). The claims have been amended to recite that the adjustable termination path is switchable between an open emitter pulldown and matching impedance or a line to line impedance of the first and second data bus. This is not disclosed or suggested by Decuir. Thus, claims 3, 4 and

12 should be allowable. Claims 2 and 11 are dependent, and should be allowable in view of at least the dependencies.

For all of the foregoing reasons, it is respectfully submitted that all of the claims now present in the application are clearly novel and patentable over the prior art of record, and are in proper form for allowance. Accordingly, favorable reconsideration and allowance is respectfully requested. Should any unresolved issues remain, the Examiner is invited to call Applicants' attorney at the telephone number indicated below.

A check in the amount of \$468.00 is enclosed on account of the additional claim fees. The Commissioner is hereby authorized to charge payment for any fees associated with this communication or credit any over payment to Deposit Account No. 16-1350.

Respectfully submitted,

  
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11-4-02  
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#### CERTIFICATE OF MAILING

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Application No.: 09/809,235

**Marked Up Claim(s)**

3. (Twice Amended) A device for changing a termination voltage of a differential data bus, the differential data bus comprising a first data bus and a second data bus, the device comprising:

a first adjustable termination path connectable to the first data bus;

a second adjustable termination path connectable to the second data bus; and

a switch connectable in parallel with the first adjustable termination path and the second adjustable termination path;

wherein the first adjustable termination path is [50 ohm to -2 volts or 100 ohms] switchable between [the] an open emitter pulldown and matching impedance or a line to line impedance of the first data bus and the second data bus.

4. (Twice Amended) A device for changing a termination voltage of a differential data bus, the differential data bus comprising a first data bus and a second data bus, the device comprising:

a first adjustable termination path connectable to the first data bus;

a second adjustable termination path connectable to the second data bus; and

a switch connectable in parallel with the first adjustable termination path and the second adjustable termination path;

wherein the second adjustable termination path is [50 ohm to -2 volts or 100 ohms] switchable between [the] an open emitter pulldown and matching impedance or a line to line impedance of the first data bus and the second data bus.

12. (Twice Amended) An apparatus for changing terminations in an emitter coupled logic (ECL) transceiver having a differential data bus, the apparatus comprising:

a variable termination connectable to the differential data bus, the variable termination comprising a first termination path or two second termination paths, the differential data bus having:

a first data bus connectable to the ECL transceiver; and

a second data bus connectable to the ECL transceiver;

wherein the first termination path further [substantially comprises 50 ohms to -2 volts or 100 ohms] is switchable between [the] an open emitter pulldown and matching impedance or a line to line impedance of the first data bus and the second data bus.